

SPECIFICATION

FOR LCD MODULE

MODEL NO:	TM12864G3CCWGWA-1
CUSTOMER:	
CUSTOMER P/N.	
VERSION	V1.0
CUSTOMER	
APPROVED	

- □ Preliminary specification
- Final specification

PREPARED BY	CHECKED BY	VERIFIED BY QA DEPT.	APPROVED BY

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REVISION RECORD

Version	Page	Revision Items	Name	Date
1.0		First release	Chenrong	2008.1.15



TABLE OF CONTENTS

	Page
1. Description	3
2. Features	3
3. Absolute maximum ratings	4
4. Mechanical Characteristics	4
5. Circuit	6
6. Interface description	8
7. Instruction Code & Timing characteristics	9
8. Electrical Specifications	12
9.LED backlight characteristics	12
10.Optical Characteristics	13
11.Reliability	17
12. Quality level	19
13 Precautions for Use of LCD Modules	25
14. LCD Module Part Numbering System	27



1 Description

The TM12864G3CCWGWA-1, LCM unit consists of 128×64 dot-matrix LCD, and a single-chip ST7567 which incorporates LCD controller and common/segment driver. This module can be connected directly to microprocessor with 4-line serial interface. Built in a RAM with 65×132 -bit capacity for storing the display data, so the unit can efficiently display the desired content under microprocessor control.

- Wide viewing direction.
- Wide Operating temperature.
- Requirements on environmental protection: RoHS.
- ◆ Temperature compensate function option

2 Features

Item	Contents				
I CD turns	FSTN				
LCD type	Positive				
LCD Duty	1/64				
LCD Bias	1/9				
Polarizer	Transflective				
LCD background color	White				
Segment color	Black				
Backlighting	LED				
Backlighting type	Edge				
Backlighting color	White				
Backlighting drive	60mA				
View direction	6:00 (wide view direction)				
Operating temperature	-20℃ ~ +70℃				
Storage temperature	-30℃ ~ +80℃				
Controller	ST7567				
Frame					
Technology	COG				
Power supply	VDD=3.0V				
Data Transfer	Serial				

Notes:

• Contrast ratio can slightly change with temperature and driving voltage.



3 Absolute maximum ratings

(Without LED backlighting ,Ta=25°C)

Parameter	Symbol	Min	Max	Unit	Remark
Logic circuit supply voltage	VDD	-0.3	3.6	V	
LCD driving voltage	VLCD	-0.3	9.5	V	
Operating temperature range	Тор	-20	+70	℃	No
Storage temperature range	Tst	-30	+80	°C	Condensation

Note:

- LCD operating voltage V_{LCD}=V0−XV0
- If the module is above these absolute maximum ratings. It may become permanently damaged.

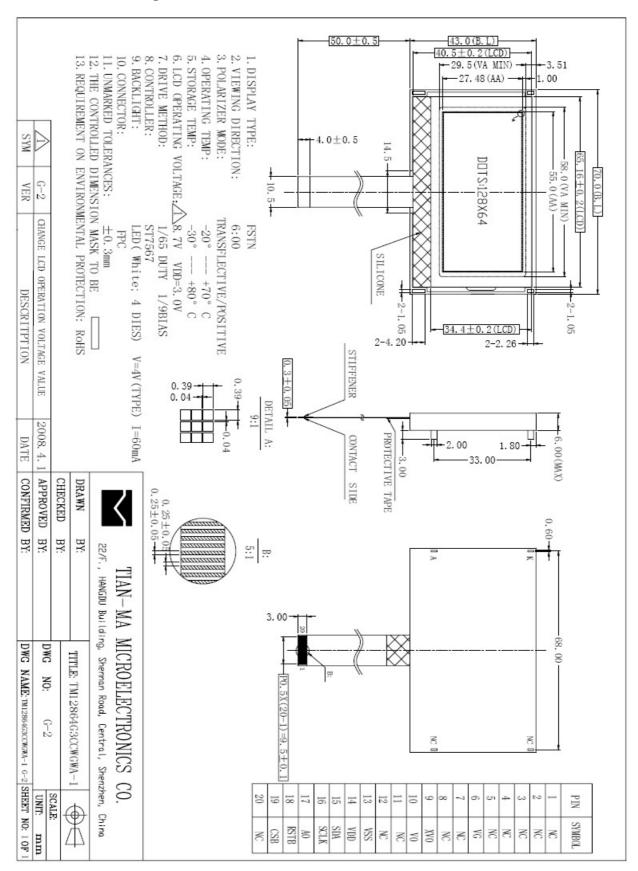
4 Mechanical Characteristics

4.1 Mechanical features

Parameter	Standard Value	Unit
Display type	Dot -matrix	
Character size (W×H)		mm
Number of dots/characters (W×H)		
View area (W×H)	58.00 × 29.5	mm
Active Area (W×H)	55.0 × 27.48	mm
Dot Size (W×H)	0.39 × 0.39	mm
Dot Pitch (W×H)	0.43 × 0.43	mm
Module size(W×H×D)	70.00 × 43.00 × 9.00 (MAX)	mm
Module total weight (approx)	26.0	g
Module outline dimensions	Refer to page 5-"Outline drawing"	



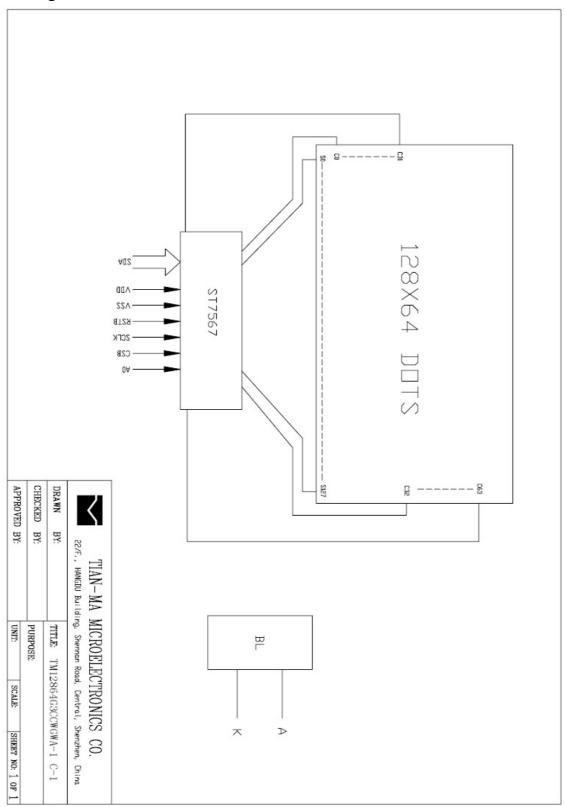
4.2 Mechanical drawing.





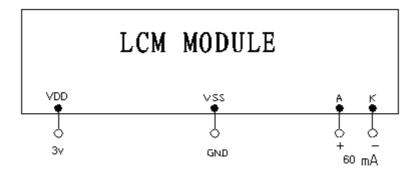
5 Circuit

5.1 Block Diagram





5.2 Recommend power supply circuit



Note:

 The maximum value of VDD should be less than 3.3 V to avoid permanent damage for LCD module



6 Interface description.

Pin No.	Symbol	I/O	Description
1	VSS	0V	GND and connect capacitor2 (-)
2	VDD	3.0V	Power supply voltage for logic and LCD(+)
3	CSB	I	Chip select
4	SCLK	I	Serial clock signal
5	A0	I	Data read/write enable signal
6	SDA	I	Serial data
7	RSTB	I	Hardware reset
7	V0	-	Vop test port (+) and connect to capacitor1 (+)
8	XV0	-	Vop test port (-) and connect to capacitor1 (-)
9	VG	-	Connect to capacitor2 (+)



7 Instruction Code & Timing characteristics

7.1 COMMAND

The module TM12864G3CCWGWA-1 contains the controller-ST7567. The table below lists the types of commands, including f the code of each command. More details refer to ST7567 data sheet please.

INSTRUCTION		COMMAND BYTE						DESCRIPTION			
INSTRUCTION	Α0	(RWR)	D7	D6	D5	D4	D3	D2	D1	D0	DESCRIPTION
(1) Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=1, display ON D=0, display OFF
(2) Set Start Line	0	0	0	1	S5	S4	S3	S2	S1	S0	Set display start line
(3) Set Page Address	0	0	1	0	1	1	Y3	Y2	Y1	Y0	Set page address
(4)	0	0	0	0	0	1	Х7	X6	X5	X4	Set column address (MSB)
Set Column Address	0	0	0	0	0	0	Х3	X2	X1	X0	Set column address (LSB)
(5) Read Status	0	1	0	MX	D	RST	0	0	0	0	Read IC Status
(6) Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write display data to RAM
(7) Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read display data from RAM
(8) SEG Direction	0	0	1	0	1	0	0	0	0	MX	Set scan direction of SEG MX=1, reverse direction MX=0, normal direction
(9) Inverse Display	0	0	1	0	1	0	0	1	1	INV	INV =1, inverse display INV =0, normal display
(10) All Pixel ON	0	0	1	0	1	0	0	1	0	AP	AP=1, set all pixel ON AP=0, normal display
(11) Bias Select	0	0	1	0	1	0	0	0	1	BS	Select bias setting 0=1/9; 1=1/7 (at 1/65 duty)
(12) Read-modify-Write	0	0	1	1	1	0	0	0	0	0	Column address increment: Read:+0 , Write:+1
(13) END	0	0	1	1	1	0	1	1	1	0	Exit Read-modify-Write mode
(14) RESET	0	0	1	1	1	0	0	0	1	0	Software reset
(15) COM Direction	0	0	1	1	0	0	MY		-	-	Set output direction of COM MY=1, reverse direction MY=0, normal direction
(16) Power Control	0	0	0	0	1	0	1	VB	VR	VF	Control built-in power circuit ON/OFF
(17) Regulation Ratio	0	0	0	0	1	0	0	RR2	RR1	RR0	Select regulation resistor ratio
(18) Set EV	0	0	1	0	0	0	0	0	0	1	Double command!! Set
(10) 001 EV	0	0	0	0	EV5	EV4	EV3	EV2	EV1	EV0	electronic volume (EV) level
(40) 0-4 B :	0	0	1	1	1	1	1	0	0	0	Double command!!
(19) Set Booster	0	0	0	0	0	0	0	0	BL1	BL0	Set booster level: 00=4X, 01=5X, 10=6X
(20) Power Save	0	0	Compound Command			Display OFF + All Pixel ON					
(21) NOP	0	0	4	1	1	0	0	0	1	1	No operation
(22) Test	0	0	1	1	1	1	1	1	1	-	Do NOT use. Reserved for testing.

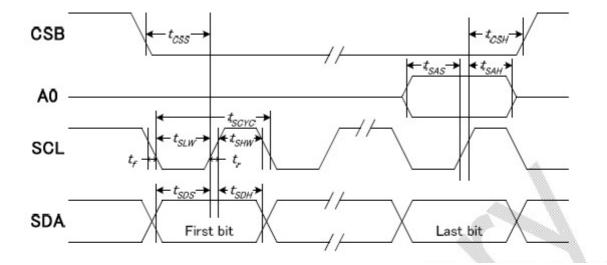
Note: Symbol "-" means this bit can be "H" or "L".



7.2 Interface Timing characteristics

Note: Please refer to IC: ST7567 data sheet for more details.

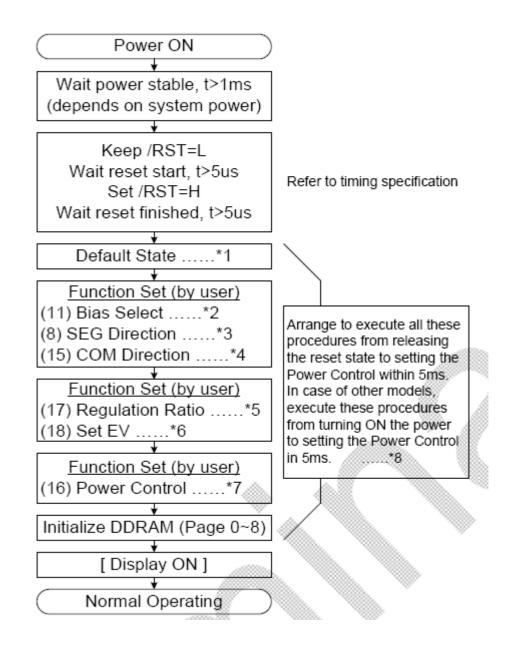
System bus timing for 4-line serial interface





7.3 Initialization flow map

(For 4-line serial Interface)





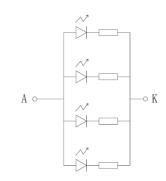
8 Electrical characteristics

VDD=5.0V, $V_{SS}=0V$, $Ta=25^{\circ}C$

Parameter		Symbol	Condition	MIN	TYP	MAX	UNIT
Logic circuit supply volta	age	VDD		2.8	3.0	3.2	
Power supply LCD	V _{LCD}		8.5	8.7	8.9		
Input voltage for logic circuit	"H"level	V _{IH}					V
	"L"level	V _{IL}	VDD=5.0V				V
Output voltage for	"H"level	V _{OH}					
logic circuit	"L"level	V _{OL}					
Logic power supply current		I _{CC}	-			8.0	mA
Used driver IC		ST7567 OF SITRONIX					

9 LED backlight characteristics

Ta=25°C



Circuit diagram (LED 1x4=4 dies)

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Forward voltage	V_{f}	I _f =60mA	3.8	4.0	4.2	٧
Luminous intensity*	Вр		280	380	510	cd/m ²
Luminous Uniformity*	△Вр	I _f =60mA	75			%
Color coordinate	Х		0.265		0.315	
Color coordinate	Υ		0.265		0.315	

Note:

- Measured at the bare LED backlight unit.
- If the backlight is above these maximum ratings for long time, the service life of the LED backlight will reduce or it will cause poor reliability.



10 Optical Characteristics

10.1 Optical Characteristics (without backlight)

Ta=25 □

Parameter		Symbol		Ratings			Measuring	Reference	
Farai	neter	Symbol	Min	Type	Max.	Unit	Temp.	heierence	
Operating	g voltage	Vo	8.5	8.7	8.9	V	25℃	(Note10-1)	
Frame fr	equency	f		77		Hz		(Note10-2)	
Contra	st ratio	Cr(θ=20°, Φ=90° or 270°)	3				25□	(Note10-3)	
	Turn on	+			250	ms	25□	(Note10-4)	
Response	Tulli oli	t _{on}					0 🗆		
time	Turn off	+			200	me	25□		
	Turri oii	t_{off}				ms	0 🗆		
Viewing	Up-down	<i>θ</i> 1 (<i>Φ</i> =90° or 270°)		-30~40		deg	25□	(Note10-5)	
angle (Cr≥2)	Left-right	<i>θ</i> 2 (<i>Φ</i> =0° or 180°)		-30~30		deg	25□	(1101610-3)	

(Note10-1) The maximum and minimum ratings don't mean the LCD works well in the whole range of Vo. Vo must be adjusted to optimize the viewing angle and contrast. Refer to definition of drive voltage, refer to 10.2.

(Note10-2) The frequency shouldn't be too low to avoid flicker. Refer to definition of drive voltage, refer to 10.2.

(Note10-3) Refer to 10.2/10.3/10.4/10.5.

(Note10-4) The selected state is dark and non-selected state is white (or bright) with positive type, reversely the selected state is white (or bright) and non-selected state is dark with negative type. Refer to 10.6 definition of response time.

(Note10-5) Generally the viewing direction is 6:00 or12:00, sometimes 3:00 or 9:00. The range of left to right and up to down based on Cr=2 show the viewing angle. Viewing angle range isn't the range of defects inspection. Refer to 10.4.



10.2 Definition of drive voltage

(1) Definition of drive voltage and waveform

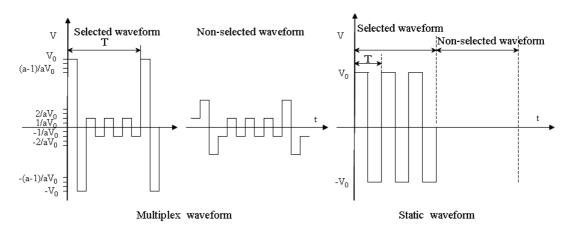


Fig.1 Definition of drive voltage and waveform

Operating voltage: V_o Frame frequency: f=1/T

Duty: 1/N Bias: 1/a

(2) Operating voltage: Vo

TIANMA can evaluate whether the LCD can be redesigned to obtain customer preferable performance if customer's LCD drive voltage isn't adjustable.

10.3 Optical characteristics measurement equipment and method

The setup and test method are showed in fig.2. Test methods are different according to different illumination mode.

Transmissive mode: light resource is placed at the back of LCD.

Reflective mode and transflective mode: light resource is placed at the front side of LCD.

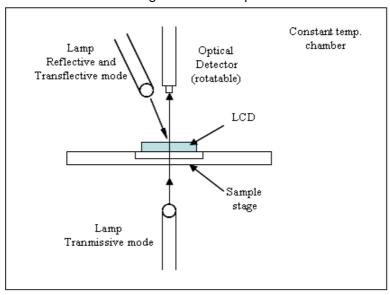


Fig.2 Optical characteristics measurement equipment



The chamber temperature, light resource and driving signal should be stable before testing. If test the characteristics under high or low temperature, the test system should be stable for more than 10 minutes before testing.

10.4 Definition of viewing direction

Refer to the graph below marked by θ and Φ

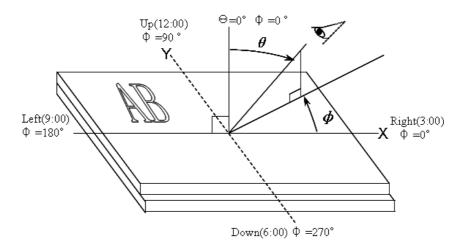


Fig.3 Definition of viewing direction

10.5 Definition of contrast ratio

Contrast ratio can be calculated by the formula (10-1) below for positive type. If the LCD is negative type, $Cr(\theta, \Phi)$ is equal to luminance $(\theta, \Phi, \text{non-selected state})$ divided by luminance $(\theta, \Phi, \text{selected state})$. Note3-4 shows the relationship between selected state, non-selected state and bright state, dark state.

$$\operatorname{Cr}(\theta, \phi) = \frac{L_2}{L_1} = \frac{\operatorname{Luminance}(\theta, \phi) \text{ (Bright state)}}{\operatorname{Luminance}(\theta, \phi) \text{ (Dark state)}}$$
(10-1)

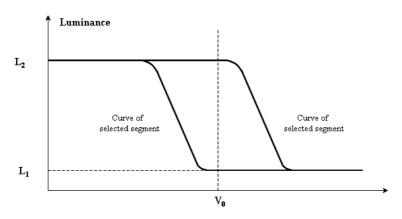


Fig.4 Electro-optical characteristic (EOC) graph (positive type)

10.6 Definition of response time

Turn on time (rise time): $t_{on} = t_d + t_r$ (from non-selected state to selected state) Turn off time (fall time): $t_{off} = t_D + t_R$ (from selected state to non-selected state)



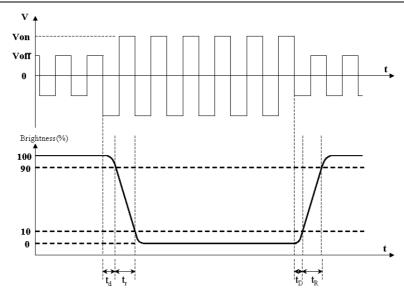


Fig.5 Definition of response time (positive type)

3.7 Definition of viewing angle

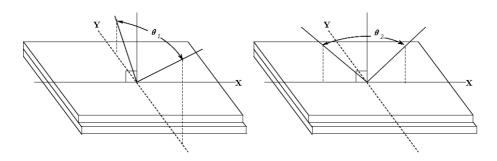


Fig 6 Definition of viewing angle

 θ_1 ——range of viewing angle from up to down

 θ_2 ——range of viewing angle from left to right.



11 Reliability

11.1 Content of Reliability Test

Ta=25□

No	Test Item	Test condition	Criterion	
1	High Temperature Storage	80℃±2℃ 120H Restore 2H at 25℃ Power off		
2	Low Temperature Storage	-30°C±2°C 120H Restore 2H at 25°C Power off		
3	High Temperature Operation	70℃±2℃ 120H Restore 2H at 25℃ Power on		
4	Low Temperature Operation	-20°C±2°C 120H Restore 4H at 25°C Power on	After testing, cosmetic and electrical defects	
5	High Temperature & Humidity Operation	Temperature & Humidity Operation		
6	Temperature Cycle	-30°C → 25°C → 80°C 30min 5min 30min after 10cycle, Restore 2H at 25°C Power off		
7	Vibration Test	10Hz~150Hz, 100m/s ² , 120min		
8	Shock Test	Half-sine wave,300m/s ² ,11ms		
9	Drop Test(package state)	800mm, concrete floor,1corner, 3edges, 6 sides each time	1.After testing, cosmetic and electrical defects should not happen. 2.the product should remain at initial place 3. Product uncovered or package broken is not permitted.	

Notes:

- 1. Each test item applies for a test sample only once, the test sample can not be used again in any other test item.
- 2. The test sample is inspected after 2 hours or more storing at room temperature and room humidity after each test item is finished.
- 3. The criteria refer to 11.2.



11.2 Inspection of criteria

Remark NO.	Content				
1	Functional test is OK. Missing Segment, shorts, unclear segment, non-display, display abnormally, liquid crystal leak are unallowable.				
2	After testing, cosmetic defects should not happen, no low temperature bubbles, seal loose and fall, frame rainbow, ACF bubble growing are unallowable in the appearance test.				
3	Total current consumption should not be over 10% of initial value.				
4	After tests being executed, Contrast must be larger than 70% of its initial value prior to the tests.				
5	No glass crack, chipped glass, end seal loose frame crack and so on.				
6	No structure loose and fall.				



12 Quality level

12.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially degrade usability for product applications. All functional defects: no display display abnormally open or missing segment short circuit, missing component, outline dimension beyond the drawing, and progressive defects and those affecting reliability.

Miner defects (MI): A minor defect refers to a defect which is not considered to be able substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation. Black spot, White spot, Bright spot, Pinhole, Black line, White line, Contrast variation, Bubble(Bubble in the cell is not included), Glass defect, and Polarizer defect beyond the standard as follows.

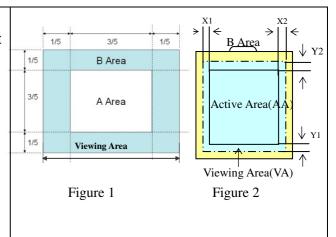
12.2 Definition of inspection range

For dot defect of TFT LCD which is not smaller than 3 inches, dividing three areas to make a judgment (according to figure 1).

A zone : center of viewing area B zone : periphery of viewing area C zone : Outside viewing area

For other defects, dividing two areas to make a judgment (according figure 2).

A zone : Inside Viewing area B zone : Outside Viewing area



12.3 Inspection items and general notes

General notes	□ Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and TIANMA. □ Viewing area should be the area which TIANMA guarantees. □ Limited sample should be prior to this Inspection standard. □ Viewing judgment should be under static pattern. □ Inspection conditions Inspection distance: 250 mm (from the sample) Temperature: 25±5 °C				
	ees in 12 o'clock direction (all defects in viewing area should be				
	inspected from this direction)				
	Pinhole, Bright spot, Black				
Inspection	spot, White spot, Black line,	The color of a small area is different from the remainder. The			
items	White Line, Foreign particle,	phenomenon doesn't change with voltage			
	Bubble				
	Contrast variation	The color of a small area is different from the remainder. The			
phenomenon ch		phenomenon changes with voltage			
Polarizer defect Scratch, Dirt, Particle,		Scratch, Dirt, Particle, Bubble on polarizer or between			
	Folarizer defect	polarizer and glass			
	Dot defect (TFT LCD)	the pixel appears bright or dark abnormally when display.			





Functional defect	no display, display abnormally, open or missing segment, short circuit, False viewing direction
Glass defect	Glass crack, Shaved corner of glass, Surplus glass
Segment defect	Pin holes or cracks in segment, Transformation of segment
PCB defect	Components assembly defect

12.4 Outgoing Inspection level

Outgoing Inspection	Inspection conditions –		Inspection				
standard			Max.	Unit	IL	AQL	
Major Defects	See 13.3 general notes	otes See 13.5		5	II	0.65	
Minor Defects	See 13.3 general notes	See		5	II	1.5	
Note: Sampling standard conforms to GB2828							

12.5 Inspection Items and Criteria

Judgment standard										
	Inspec	tion items	Category			Acceptable number				
	шороо					A zone		B zone		
	Black spot, White		A Φ≦0.10		0≦0.10	Neglected		2 20110		
	spot, Bright Spot,	b	B 0.10<Φ≦0.15			2				
	Pinhole, Foreign		С		5<Φ≦0.20		 1			
1	Particle, Bubble and Particle in or	a	D).20<Ф	()	Neglected		
	on glass, Scratch on glass	$0 \leftarrow 0 \Rightarrow 0$ $\Phi = (a+b)/2(m)$	To	tal defect	ive point(B,C)	(3			
			Α	٧	<i>I</i> ≦ 0.01	Neglecte	ed			
	Black line, White line, Bubble and	Width	В	0.01 <w L≦3.0</w 	≦0.03	2	2			
2	Particle Between Polarizer and glass, Scratch on	L:Length(mm)	С	0.03 <w L≦3.0</w 	≦0.05	-	1	Neglected		
	glass, Scratch on	\K	D		.05 <w< td=""><td>(</td><td>)</td><td></td></w<>	()			
	giass		Total defective point(B,C)		3		1			
		1	¬ A Φ≦0.2		A		⊅≦0.2	Neglected		
	Contrast	b				!<Φ≦0.3		2	Neglecte	
3	variation	\longleftrightarrow	С	0.3	<Φ≦0.4	-	1	d		
	variation	$\Phi = (a+b)/2(mm)$	D		0.4<Ф)			
			Total defective point(B,C) 3		3					
4	Dot defect (if TFT	TFT LCD is smaller	LC	D Class	Defect	Defect A zone		B zone		
	LCD is used)	than 3 inches			Bright dot	1				
				A Dark dot		2				
					Total	2		Neglecte		
			B LCD Class		Bright dot		2	d		
					Dark dot Total		3 1			
		TFT LCD between			Defect	A zone	B zone	C zone		
		3~10.4 inches		D Olass	Bright dot	1	1	O ZONG		
			В		Dark dot					
					Total	1	2	Neglecte		
					Bright dot	2	1 2	d		
					Dark dot	2	3			
					Total	1	<u> </u>			



	Notes						
		Dark dot: in R、G、B	ght dot: in R、G、B or dark display figure, the pixel appears bright. rk dot: in R、G、B or white display figure, the pixel appears dark. fect area must be less than an half size of the dot.				
5	Bubble inside cell			any size		none	none
	Polarizer defect (if	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Refer to item 1 and item 2.				
6	Polarizer is used)	Bubble, dent and	Α	Ф≦0.3		Neglected	
		convex	В	0.3<Φ≦0.7	7	2	Neglecte
			С	0.7<Ф		0	d
			То	tal defective point((B,C)	2	
	Surplus	Stage surplus glass	b≦	0.3mm			
7	glass	Surrounding surplus glass	Should not influence outline dimension and assemb			embling.	
8	Open segment or op	oen common	Not	permitted	d		
9	Short circuit		Not permitted				
1	False viewing direct	ion	Not permitted				
11	Contrast ratio uneve	en	According to the limit specimen				
1 2	Crosstalk		According to the limit specimen				
1 3	Black /White spot(di	splay)	Refer to item 1				
1 4	Black /White line(dis	splay)	Ref	er to item 2			
1 5		- + + a		not counted	Max	x.3 dots allowed	
				x<0.1mm	0.1	1mm≤x≤0.2mm	
	Pin holes	a-1 1-		X=	(a+b)/2		May 2
	and cracks in segment	-D 4		not counted	Max	x.2 dots allowed	Max.3 dots allowed
				A<0.1mm	0.1	Imm≤A≤0.2mm D<0.25mm	



1 6	Transformation of segment	4	not counted	Max.1 defects allowed each segment	
			x<0.1mm	0.1mm≤x≤0.2mm	
			X=	(a+b)/2	Max.3 defects
		D-++-a	not counted	Max.1 defects allowed each segment	allowed
			a<0.1mm	0.1mm≤a≤0.2mm D>0	
			a=measure	/≤a≤1.2W ed value of width al value of width	Max.2 defects allowed

	Inspection items			Judgment standard
		inspection terms		Category(application: B zone)
17	Glass	☐The front of lead terminals	Α	If a □ t and b □1.0, c is not limited
	defect crack		В	a □ t, 1□b□2mm, c□3mm
		b	С	If glass crack cover alignment mark and patterns, b □ 0.5 mm.
		w t a c	D	Crack at two sides of lead terminals should not cover patterns and alignment mark
		②Surrounding crack—non-contact side seal c b a t Inner border line of the seal Outer border line of the seal		b < Inner borderline of the seal



3 Surrounding crack— contact side seal c h a Inner border line of the seal Outer border line of the seal		b < Outer borderline of the seal
(4)Corner	Α	a □ t, b □ 3.0, c □ 3.0
w b c		s crack should not cover patterns u and nent mark and patterns.

Component soldering: No cold soldering, short, open circuit, burr, tin ball The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1); the sheet component deviation: Pin deviates from the pad and contact with the near components is not permitted (Pic.2) lead defect: The lead lack must be less than 1/3 of its width; The lead burr must be less than 1/3 of the seam; Impurities connect with the near leads is not permitted	Component Lead L1>0
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Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted	Soldering tin is not permit in this area Soldering tin is not permit in this area Socket Base Board
Glue on root of the speaker receiver and motor lead: The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.	Glue Lead PCB Insulative coat



13 Precautions for Use of LCD Modules

13.1 Handling Precautions

- 13.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 13.1.2 Liquid in LCD is hazardous substance, if the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, thoroughly and promptly wash it off using soap and water.
- 13.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 13.1.4 Don't touch, push or rub the exposed polarizer covering the display surface of the LCD module with anything harder than an HB pencil lead, the polarizer is soft and easily scratched, handle it carefully.
- 13.1.5 Don't put or attach anything on the display area to avoid leaving any marks on.
- 13.1.6 If the display surface is contaminated or becomes dusty, breathe on the surface and gently wipe it with a soft dry cloth. do not scrub hard to avoid damage the surface. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 13.1.7 Do not attempt to disassemble the LCD Module.
- 13.1.8 If the logic circuit power is off, do not apply the input signals.
- 13.1.9 Avoid using the same display pattern long time (continuous ON segment). Software must be prepared so that the pattern will be changed
- 13.1.10 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body and electric appliances when handling the LCD Modules. It is preferable to use conductive mat on table and wear cotton clothes or conductive processed fibre. Synthetic fibre is not recommended.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be careful and slow when peeling off this protective film since static electricity may be generated. It is recommended to use ionic fan or machine when operating. It is recommended to remove the protection foil slowly (> 3 sec.).
 - e. It is preferable to wear gloves etc, to avoid damaging the LCD. Please do not touch electrodes with bare hands or avoid any other contamination.





13.2 Storage precautions

- 13.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 13.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 5° C \sim 40 $^{\circ}$ C

Relatively humidity: ≤80%

- 13.2.3 The LCD modules should be stored in a clean environment or room, free from acid, alkali and harmful gas.
- 13.2.4 Store the module in anti-static electricity container and without any physical load.

13.3 Transportation precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

13.4 Soldering

- 13.4.1 Use the high quality solders, only solder the I/O terminals.
- 13.4.2 No higher than 280 □ and time less than 3-4 second during soldering.
- 13.4.3 Rewiring: no more than 3 times.
- 13.4.4 when you remove connector or cable soldered to I/O terminals, please confirm that solder is fully melted. If you remove by force, electrodes at I/O terminals may be damaged (or stripped off). It is recommended to use solder suction machine.





14. LCD Module Part Numbering System

TM 12864 G 3 C C W G W A-1

1 2 3 4 5 6 7 8 9

NO.	Explanation	
1	TIANMA module indicating	
2	Module type: Dot-matrix, 128 \times 64,	
3	TIANMA module series (A,B,C,DG3)	
4	LCD type	
	С	Positive, FSTN
5	Backlight type	
	С	Transflective, LED
6	Temperature range	
	W	Wide temperature
7	IC Package Technology	
	G	COG
8	Viewing Angle	
	W	Wide view
9	Version number	
	A-1	